

## PALM INTRANET

Application#	Patent#	PG Pub#	Status	Date Filed	Title	Examiner Name	Inventor Name
10695498	7450530	20040141497	150	10/29/2003	CROSS LINK MULTIPLEXER BUS CONFIGURED TO REDUCE CROSS-TALK	JAIN,RAJ	AMIRICHIMEH, ABBAS
10695458	7450529	20040141531	150	10/29/2003	CROSS LINK MULTIPLEXER BUS	JAIN,RAJ	AMIRICHIMEH, ABBAS
60540295	Not Issued		159	01/30/2004	TRANSCIVER DEVICE WITH A TRANSMIT CLOCK SIGNAL PHASE THAT IS PHASE-LOCKED WITH A RECEIVE CLOCK SIGNAL PHASE		AMIRICHIMEH, ABBAS
10813235	Not Issued	20050169417	071	03/31/2004	SYSTEM AND METHOD OF PHASE-LOCKING A TRANSMIT CLOCK SIGNAL PHASE WITH A RECEIVE CLOCK SIGNAL PHASE	BOCURE,TESFALDET	AMIRICHIMEH, ABBAS
10813363	Not Issued	20050169355	041	03/31/2004	TRANSCIVER DEVICE WITH A TRANSMIT CLOCK SIGNAL PHASE THAT IS PHASE-LOCKED WITH A RECEIVER CLOCK SIGNAL PHASE	BOCURE,TESFALDET	AMIRICHIMEH, ABBAS
12253851	Not Issued		019	01/01/0001	CROSS LINK MULTIPLEXER BUS		AMIRICHIMEH, ABBAS

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